

SPECIAL SESSION (Hybrid Mode): Energy-Efficient AI: Co-Design of Hardware and Machine Learning Model

ORGANIZERS



Prof. Amitava Nag (Lead)

Central Institute of Technology
Kokrajhar, Assam, India



Dr. Nabajyoti Mazumdar

IIIT Allahabad
Prayagraj, Uttar Pradesh, India



Dr. Suman Sau

Siksha 'O' Anusandhan University
Bhubaneswar, Odisha, India

TOPICS (but are not limited to)

- AI for Energy Management, Smart Grids, and Environmental Sustainability
- Explainable & Trustworthy AI
- Hardware–Software Co-Design for Energy-Efficient Deep Learning
- Embedded and Edge AI on RISC-V, FPGA, and ASIC Platforms
- AI for Smart Healthcare & Digital Health
- AI-Driven Cybersecurity & Privacy
- Generative AI & Large Language Models
- Edge AI, Cloud Computing & Federated Learning
- Emerging Intelligent Computing Technologies
- ML and Emerging Technologies for Cyber Security

IMPORTANT DATES

Submission Deadline
October 01, 2026

Acceptance Notification
November 1, 2026

Registration Due
November 10, 2026

Main Sessions
November 26–28, 2026

Viksit Bharat @ 2047 • Towards a Developed India
Hybrid Mode • Energy-Efficient AI • Hardware–Software Co-Design